

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)	Complete if Known	
	Application Number	10/075659
	Filing Date	February 13, 2002
	First Named Inventor	Mosley, Larry
	Group Art Unit	2827
	Examiner Name	Vigushin, John
Sheet 1 of 1	Attorney Docket No: 884.209US2	



US PATENT DOCUMENTS						
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date if Appropriate

FOREIGN PATENT DOCUMENTS						
Examiner Initials *	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
<i>pl</i>		"Capacitor for Multichip Modules", IBM Technical Disclosure Bulletin, Vol. 20, Issue 8, (01/01/1978), 3117-3118	

EXAMINER

John B. Vigushin

DATE CONSIDERED

July 24, 2003

Substitute Disclosure Statement Form (PTO-1449)
 * EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 808. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached

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